

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Toshiyuki Nishihara, et al

Art Unit: 2186

Application No.: 10/798,413

Conf. No. 6020

Filed: March 11, 2004

For: DATA READING METHOD, DATA WRITING
METHOD, AND SEMICONDUCTOR
MEMORY DEVICE

SECOND INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Pursuant to 37 CFR §1.56, the attention of the Patent and Trademark Office is hereby directed to the information item(s) listed on the attached PTO-1449. Unless otherwise indicated herein, one copy of each item(s) is attached. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the item(s) be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

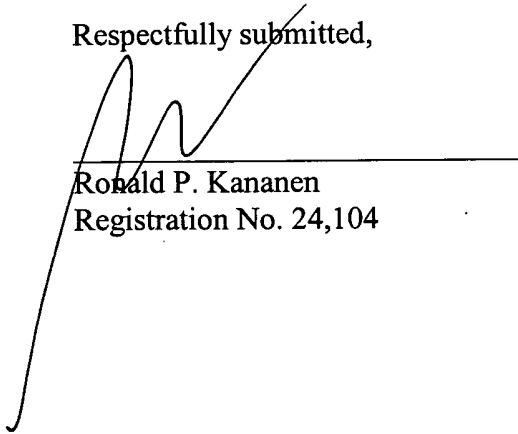
- ☒ 1. This Information Disclosure Statement is being filed (a) within three months of the U.S. filing date or the date of filing a CPA, OR (b) before the mailing date of a first Office Action on the merits in the present application, or (c) accompanies a Request for Continued Examination. No certification or fee is required.
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- ☐ a. I hereby certify that each item of information contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this Information Disclosure Statement. 37 CFR §1.97(e)(1).
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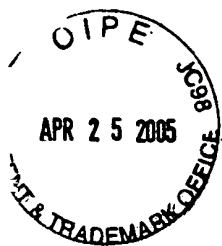
Dated: April 25, 2005

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PTO/SB/08a/b (08-03)

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Substitute for form 1449A/B/PTO			Complete if Known		
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)			Application Number	10/798,413	
			Filing Date	March 12, 2004	
			First Named Inventor	Toshiyuki Nishihara	
			Art Unit	2186	
			Examiner Name	Not Yet Assigned	
Sheet	1	of	1	Attorney Docket Number	SON-2939

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
	AA				

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³ -Number ⁴ -Kind Code ⁵ (if known)				
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NON PATENT LITERATURE DOCUMENTS			
Examiner Initials	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	CA	2001 Symposium on VLSI Circuit Digest of Technical Papers, "A Bit-Line GND Sense Technique for Low-Voltage Operation FeRAM", Kawashima, et al.	
	CB	ISSCC 2003 Session 16, "Non-Volatile Memory", Sakimura, et al	
	CC		
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